

Assembled listings of the source code are the preferred format for code review. Assembled listings are generated by the a51 assembler and assume a 132 column printer. On each line of text, listings include the following information:

1. Leftmost column reports the offset from the beginning of the segment. The information combined with the memory map (*.m51) file can be used to determine the exact location of code in address-space.
2. The next column reports the actual code text generated. Some code text, marked with an 'F' to the right, is filled-in by the linker and is represented as zeros. The code text is helpful in verifying the code found at any location.
3. Line numbers indicate source code actually assembled (not line number from source file). During the code walkthru, we will refer to line numbers as specified by the assembled listing. A line number with a '+' sign, such as 4579+1, indicates the line was generated by expanding a macro.
4. Source code occupies all columns to the right of line numbers, in general laid-out as follows: Labels in column 1, opcode mnemonics in column 5, operands starting in column 12, comments starting in column 31.

The DCE source code is a collection of five source code modules, a file included by all modules, and a set of linker specifications. The five source files are Initialz.a51, Executiv.a51, Commands.a51, Internal.a51, and Patchcst.a51; all five source files reference the include file Common.inc, which defines the memory map, hardware layout, macro definitions, public and external declarations for all modules, and miscellaneous equates (constant declarations). Source code modules declare one or more code segments. The linker specifications describe where each segment is to be located in 64k codespace. Linker specifications are named bootmode, lower, upper, and partial. Some software functionality depends on the location of the code executed.