

DCE Memory Map

5/5/00 KH

Address	Names	Read/Write	Note
0000-1FFF	ROM0	R	~PSEN=0 & ~PROM1_ON=0
2000-3FFF	ROM1	R	~PSEN=0 & ~PROM1_ON=0
0000-7FFF	ROM (Non-Flight)	R	~PSEN=0 & ~PROM1_ON=0
0000-3FFF	LOWRAM	R/W	~PSEN=1 or ~PROM1_ON=1
2000-2FFF	LED (Non-Flight)	W	
4000-40FF	MUX	W	D0-D2 select 1 of the 8 channels
			D3=1 enables MUX0
			D4=1 enables MUX1
			D5=1 enables MUX2
			D6=1 enables MUX3
			D7=1 enables MUX4
4000-40FF	HV Status Reg	R	
4100-41FF	SPARE		
4200-42FF	Control Reg 1	R/W	
4300-43FF	Control Reg 2	R/W	
4400-44FF	DAC	W	4400-HV-Amax
			4401-DAC-A
			4402-HV-Bmax
			4403-DAC-B
4500-45FF	Bilevel Status Reg	R	
4600-46FF	LED (Non-Flight)	R	
4700-47FF	ADC	R/W	Write after Read for A/D conversion
5000-5300	Housekeeping Reg	W	5000-Byte 0
			5100-Byte 1
			5200-Byte 2
			5300-Byte 3
5000-5300	Command Reg A	R	5000-Byte 0
			5100-Byte 1
			5200-Byte 2
			5300-Byte 3
5400-5700	Command Reg B	R	5400-Byte 0
			5500-Byte 1
			5600-Byte 2
			5700-Byte 3
6000-6002	FEC Counter A	R	24 bits counter
6004-6006	DEC Counter A	R	24 bits counter
6008-600A	TLC Counter A	R	24 bits counter
6100-6102	FEC Counter B	R	24 bits counter
6104-6106	DEC Counter B	R	24 bits counter
6108-610A	TLC Counter B	R	24 bits counter
6200-62FF	PHH A RAM	R/W	
6300-63FF	PHH B RAM	R/W	
8000-FFFF	RAM	R/W	