

COS DCE BOOT FSW v1.09 Component Test Results
Requirement 5.5.3.1 CRC Background Checking on Memory Regions

Date:	February 13, 2001
Document Number:	COS-03-0035
Revision:	Initial Release
Contract No.:	NAS5-98043
CDRL No.:	N/A

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1. INTRODUCTION

1.1 PURPOSE

This document presents the Cosmic Origins Spectrograph (COS) Device Control Electronics (DCE) Flight Software (FSW) certification procedure. The purpose of this procedure is to verify that the FSW satisfies Software Requirements according to the method specified in the DCE FSW Test Plan (STP).

1.2 SCOPE

This test procedure comprises the steps necessary to verify that the FSW satisfies Software Requirements Document (SRD) paragraph 5.5.3.1 — CRC Background Checking on Memory Regions.

1.3 LIMITATIONS AND CONSTRAINTS

This test cannot be run in parallel with any other commanding activity directed at the DCE FSW (such as, for example, the periodic transmission of NOOP commands). Test hardware shall be visually inspected, and its configuration noted, prior to conducting this test.

1.4 PROCEDURE OVERVIEW

The procedure requires the `hks` tools running on the Sun SparcStation Electronic Ground Support Equipment (EGSE) whose network IP address is one of

shorty.ssl.berkeley.edu
taiyo.ssl.berkeley.edu
ginger.ssl.berkeley.edu.

Test time shall be scheduled in advance. The Test Conductor must be logged into the Unix system as user `eagcos`, and be commanding from the appropriate directory. This directory contains both the test script file and the shell script file; these two files control test execution. The test is conducted by invoking the shell script. This shell script in turn invokes the Perl 5 program `UniScript.pl`, which resides in its own distinct directory. The test procedure steps have been pre-recorded in the test script file, and are executed interpretively by the `UniScript` program. The shell script and test script are attached to this document as appendices. As `UniScript` executes the test script it sends results to the operator console and to two report files, which are also placed in the current directory. After completion of the test script, the Test Conductor can certify successful test

execution by examining the contents of the report files and determining that required outputs are present in them. Printed copies of the report files are attached to the manually completed checklist (Paragraph 4 below) as documentation of the test.

1.5 THEORY OF TEST

The contents of the 16K ROM actually consist of 6K (=6144) encoded bytes. The script downloads the ROM to UniScript Buffer 2. Since UniScript automatically re-computes the CRC of its buffers whenever they are modified, the variable \$CRC2, which contains the CRC of Buffer 2, can be compared to the value, LFMROM, present in the HK data.

1.6 TEST SCRIPT IMPLEMENTATION

1.6.1 Test Script Arguments

The script is parameterized as shown in the following Table:

Table 1-1: Parameters/Arguments for stp5_5_3_1.tst

Parameter	Meaning	Correct Argument for Version 1.09
#0	Absolute hex storage address of intermediate “scratch” area for ROM download operations	C000

These parameters must be encoded into the shell script `u` (see Appendix A).

1.6.2 Test Script Coding

The script uses standard UniScript commands and directives. The downloading of memory regions must be done via the LFDDNLOD command. However, this command has two relevant limitations:

- it downloads external RAM regions only;
- individual downloaded blocks are limited to 1K.

For these reasons, the script downloads the six 1K “pages” of ROM by six iterations of the two operations

- copy a 1K page from ROM to a “scratch” area in external RAM using the LFDCOPY command;

- download the 1K “scratch” area to Buffer 2 at the appropriate offset, using the LFDDNLOD command.

2. SPECIAL INSTRUCTIONS

2.1 QUALITY ASSURANCE

QA support is required to verify the configuration and setup environment as well as monitoring test steps and verifying results.

2.2 SAFETY

2.2.1 Personal Safety

To ensure the safety of the test personnel during test execution the guidelines contained in Paragraph 3.4, Reference [1] will be adhered to.

2.2.2 Test Article and Equipment Safety

- If access within one (1) meter of COS bench electronics is necessary, wrist straps attached to technical ground shall be used by all personnel involved in handling of any COS test article. Overcurrent and overvoltage shall be set to remove power if nominal limits are exceeded.
- Emergency Power Shutdown — If, during the COS DCE FSW test, power is ON and a severe test equipment failure results in the power system exceeding specified limits, the Test Conductor shall direct or perform shutdown of power.

2.3 CONTAMINATION

All flight hardware shall be handled with clean latex gloves; it shall be covered with clean ESD material and/or stored in a clean flow-bench.

3. SUPPORT REQUIREMENTS

3.1 PERSONNEL

Execution of the COS DCE FSW certification procedure requires the following personnel (to be completed at the Test Readiness Review (TRR)):

Test Director: _____

Test Conductor: _____

Test Technician: _____

QA: _____

3.2 TOOLS, EQUIPMENT, AND MATERIALS

The following is a list of tools, equipment, or materials required in this test. Record manufacturer and model, metrology, or property numbers of equipment used, where appropriate. Record calibration due dates where appropriate.

Boot Mode ROM: schematic **27C256**

Engineering Ground Support Equipment (see paragraph 1.4). Indicate specific configuration:

EGSE			DCE		
taiyo	shorty	ginger	ETU	DCE #1	DCE #2
	X			X	

3.3 DATA/SOFTWARE

The following files must be present:

Table 3-1: Required Program and Data Files

EGSE (shorty) Directory	File	Description
\disks\galex\users\galex\tcs\uniscrpt\	UniScript.pl	UniScript interpreter
\disks\galex\users\galex\tcs\uniscrpt\stp5_5_3_1\	u	Shell script for this procedure
Ditto	stp5_5_3_1.tst	Test script for this procedure (Appendix B)

In addition, the **hks** tools must be active. Directions for activating **hks** are given in UCB-COS-DOC-1118 (Paragraph 3.4, Reference [4]).

3.4 REQUIRED DOCUMENTATION

Reference	Document Number	Title
1	NHB 1700.1(V1-A)	<i>NASA Basic Safety Manual</i>
2	COS-03-0035	<i>DCE FSW Test Procedure 5.5.3.1 (this document)</i>
3	UCB-COS-008	<i>COS FUV Detector Software Test Plan</i>
4	UCB-COS-DOC-1118	<i>COS EGSE Startup Procedure</i>

4. PROCEDURE/TASK STEPS

4.1 PRE-OPERATION ACTIVITIES

4.1.1 Make Sure that **hks** Tools Are Active

Follow the procedure given in Paragraph 3.4, Reference [4].

4.1.2 Make Sure that the Proper ROM Is Installed

Visually verify that the ROM under test is installed: if EEPROM, in U18: if PROM, in U2 and U7.

4.1.3 Log In to the EGSE

Step	QA	Operator Entry/System Response	Description
1		C:\tcs\us> telnet shorty.ssl.berkeley.edu	Establish connection to shorty via Telnet client program
2		Login: tcs Password:	Using telnet window, login as user tcs

4.1.4 Set Current Directory

Step	QA	Operator Entry/System Response	Description
3		tcs@shorty% cd ~galex/tcs tcs@shorty% pwd /disks/galex/users/galex/tcs	Change current directory as shown

4.1.5 Slogin as eagcos

Step	QA	Operator Entry/System Response	Description
4		<pre>tcs@shorty% slogin -l eagcos shorty.ssl.berkeley.edu eagcos@shorty.ssl.berkeley.edu's password: (get from SSL personnel) Last login: Sat Oct 7 10:41:05 2000 from auntem.ssl.berke Sun Microsystems Inc. SunOS 5.8 Generic February 2000 You have mail. COS EGSE software version: devel</pre>	slogin as eagcos ; get password from SSL personnel

4.1.6 Set Current Directory

Step	QA	Operator Entry/System Response	Description
5		<pre>eagcos:shorty% cd /disks/galex/users/galex/tcs/uniscript/stp5_5_3_1 eagcos:shorty% pwd /disks/galex/users/galex/tcs/uniscript/stp5_5_3_1</pre>	Change current directory as shown

4.1.7 Ensure that Proper Files are Present

Step	QA	Operator Entry/System Response	Description
6		<pre>eagcos@shorty% ls -l Total 12 -rw-r--r-- 1 tcs eag 1398 Oct 8 18:03 stp5_5_3_1.tst -rw-r--r-- 1 tcs eag 62 Oct 9 17:44 u eagcos@shorty% more < u #!/bin/sh pkill cosnoopy perl ../UniScript.pl stp5_5_3_1 "C000,0,0,0,0,0,0" cosnoopy&</pre>	List files; the .tst file and the shell script u should be present

4.2 OPERATION EXECUTION

4.2.1 Establish Initial Test Conditions

Step	QA	Operator Entry/System Response	Description
7		<pre>eagcos:shorty% set path=(\$path ~dbb/scripts/bin)</pre>	Set path as shown to enable access to hks tools

4.2.2 Execute the Script

Step	QA	Operator Entry/System Response	Description
8		<pre>eagcos:shorty% sh u \$psstring=C000,0,0,0,0,0,0,0 Parameters are: Script File: stp5_5_3_1 #0: C000 #1: 0 #2: 0 #3: 0 #4: 0 #5: 0 #6: 0 #7: 0 Report file >/disks/galex/users/galex/tcs/uniscript/stp5_5_3_1/ stp5_5_3_1.rp1 successfully opened. Report file >/disks/galex/users/galex/tcs/uniscript/stp5_5_3_1/ stp5_5_3_1.rp2 successfully opened. Script file /disks/galex/users/galex/tcs/uniscript/stp5_5_3_1/st p5_5_3_1.tst successfully opened at level 0. "Resetting" WAIT 0: HKV0=153; HKV1=154; wc=5 LFDCRC 0x0000,NBYTES,ROM "Downloading first 64 bytes of ROM to Buffer 1" LFDCOPY 0x0000,SCRATCH,64,ROM WAIT 0: HKV0=2; HKV1=0; wc=5 WAIT 1: HKV1=1; wc=4</pre>	<p>Shell to u. You should see the accompanying output as UniScript executes</p>

Step	QA	Operator Entry/System Response	Description
		WAIT 1: HKV1=2; wc=3 LFDDNLOD SCRATCH,64 WAIT 0: HKV0=5; HKV1=4; wc=5 WAIT 1: HKV1=4; wc=4 WAIT 1: HKV1=5; wc=3 WAIT 0: HKV0=5; HKV1=6; wc=5 "Downloading Page 0" LFDCOPY PAGE0,SCRATCH,PAGSZ,ROM WAIT 0: HKV0=8; HKV1=6; wc=5 WAIT 1: HKV1=8; wc=4 LFDDNLOD SCRATCH,PAGSZ WAIT 0: HKV0=10; HKV1=8; wc=5 WAIT 1: HKV1=10; wc=4 "Downloading Page 1" LFDCOPY PAGE1,SCRATCH,PAGSZ,ROM WAIT 0: HKV0=12; HKV1=10; wc=5 WAIT 1: HKV1=11; wc=4 WAIT 1: HKV1=12; wc=3 LFDDNLOD SCRATCH,PAGSZ WAIT 0: HKV0=15; HKV1=13; wc=5 WAIT 1: HKV1=14; wc=4 WAIT 1: HKV1=15; wc=3 WAIT 0: HKV0=15; HKV1=16; wc=5 "Downloading Page 2" LFDCOPY PAGE2,SCRATCH,PAGSZ,ROM LFDDNLOD SCRATCH,PAGSZ	

Step	QA	Operator Entry/System Response	Description
		<p>WAIT 0: HKV0=19; HKV1=16; wc=5 WAIT 1: HKV1=17; wc=4 WAIT 1: HKV1=19; wc=3 WAIT 0: HKV0=19; HKV1=20; wc=5 "Downloading Page 3"</p> <p>LFDCOPY PAGE3,SCRATCH,PAGSZ,ROM</p> <p>LFDDNLOD SCRATCH,PAGSZ</p> <p>WAIT 0: HKV0=23; HKV1=20; wc=5 WAIT 1: HKV1=23; wc=4 WAIT 0: HKV0=23; HKV1=23; wc=5 "Downloading Page 4"</p> <p>LFDCOPY PAGE4,SCRATCH,PAGSZ,ROM</p> <p>WAIT 0: HKV0=25; HKV1=23; wc=5 WAIT 1: HKV1=25; wc=4</p> <p>LFDDNLOD SCRATCH,PAGSZ</p> <p>WAIT 0: HKV0=27; HKV1=25; wc=5 WAIT 1: HKV1=26; wc=4 WAIT 1: HKV1=27; wc=3 WAIT 0: HKV0=27; HKV1=28; wc=5 "Downloading Page 5"</p> <p>LFDCOPY PAGE5,SCRATCH,PAGSZ,ROM</p> <p>WAIT 0: HKV0=30; HKV1=28; wc=5 WAIT 1: HKV1=29; wc=4 WAIT 1: HKV1=30; wc=3</p> <p>LFDDNLOD SCRATCH,PAGSZ</p> <p>WAIT 0: HKV0=33; HKV1=31; wc=5</p>	

Step	QA	Operator Entry/System Response	Description
		WAIT 1: HKV1=32; wc=4 WAIT 1: HKV1=33; wc=3 WAIT 0: HKV0=33; HKV1=34; wc=5 "stp5.5.3.1 completed successfully"	

4.3 POST-OPERATION ACTIVITIES

4.3.1 Copy Reports to PC Files and Print Them

Using an FTP client, copy the **u**, **stp5_5_3_1.tst**, **stp5_5_3_1 rp1**, and **stp5_5_3_1 rp2** files to appropriate PC files. Include these files as Appendices A, B, C, and D with this completed form.

4.3.2 Complete The Test Procedure Form

Ensure that all blank fields in this report are completed correctly and submit the completed report to QA.

SUMMARY SHEET

OPERATION TITLE: _____ WOA# _____

TEST ARTICLES IDENTIFICATION (including serial and/or part numbers):

TASKS/STEPS COMPLETED: _____

LOCATION: _____

TEST STARTED:	TEST TERMINATED
TIME: _____ Hr/Min	TIME: _____ Hr/Min
DATE: _____	DATE: _____

LOGS USED: _____

ANOMALY REPORTS GENERATED: _____

COMMENTS: _____

TEST CONDUCTOR: _____
Signature/Date

QA REPRESENTATIVE: _____
Signature/Date

Appendix A. Shell Script u

```
#!/bin/sh  
kill cosnoopy  
perl ../UniScript.pl stp5_5_3_1 "C000,0,0,0,0,0,0,0"  
cosnoopy&
```

Appendix B. Test Script `stp5_5_3_1.tst`

```

; *****
; * DCE FSW Requirement 5.5.3.1 -- CRC Background Checking on Memory Regions *
; * -----
; * Verify that the CRC of a downloaded copy of the 16K ROM matches the value *
; * maintained by the DCE code (LFMROM) *
; * -----
; * Arguments: #0 = DCE "Scratch Area" = C000 in v1.07 *
; *****
;
ECHO      2
;
SYM      SCRATCH=0x#0
SYM      NSEC      =5
SYM      PAGESZ    =0x0400
SYM      PAGE0     =0x0000
SYM      PAGE1     =PAGE0+PAGESZ
SYM      PAGE2     =PAGE1+PAGESZ
SYM      PAGE3     =PAGE2+PAGESZ
SYM      PAGE4     =PAGE3+PAGESZ
SYM      PAGE5     =PAGE4+PAGESZ
SYM      NBYTES   =PAGE5+PAGESZ
SYM      ROM      =1
;
; *****
; * Move all of ROM to Buffer 2, 1K at a time *
; *****
;
DTG      3,"(0) Resetting"
WTO      "Resetting"
POR
WAIT     1
WAIT     NSEC,HK
LOG      1,LFDCBUF,LFDDIAGS,LFDSWVER,LFSBITS1,LFDOPERT,LFMROM
;
;
; #####
; # Download first 64 bytes to Buffer 1 #
; #####
;
LFDCRC   0x0000,NBYTES,ROM
WAIT     1
;
DTG      3,"(1) Downloading first 64 bytes of ROM to Buffer 1"
WTO      "Downloading first 64 bytes of ROM to Buffer 1"
LFDCOPY  0x0000,SCRATCH,64,ROM
WAIT     NSEC,HK
LFDDNLOD SCRATCH,64
WAIT     NSEC,HK
RECV     1,0,64
WAIT     NSEC,HK
LOG      1,LFDCBUF,LFDDIAGS,LFDSWVER,LFSBITS1,LFDOPERT,LFMROM,1,2
;
DTG      3,"(2) Downloading Page 0"
WTO      "Downloading Page 0"
;
LFDCOPY  PAGE0,SCRATCH,PAGESZ,ROM
WAIT     NSEC,HK
LFDDNLOD SCRATCH,PAGESZ
WAIT     NSEC,HK
RECV     2,PAGE0,PAGESZ
;LOG     1,LFDCBUF,LFDDIAGS,LFDSWVER,LFSBITS1,LFDOPERT,LFMROM,1,2
;
DTG      3,"(3) Downloading Page 1"
WTO      "Downloading Page 1"
;
LFDCOPY  PAGE1,SCRATCH,PAGESZ,ROM
WAIT     NSEC,HK

```


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```

LFDDNLOD  SCRATCH, PAGESZ
WAIT      NSEC, HK
RECV      2, PAGE1, PAGESZ
WAIT      NSEC, HK
;LOG      1, LFDCBUF, LFDDIAGS, LFDSWVER, LFSBITS1, LFDOPERT, LFMROM, 1, 2
;
DTG        3, "(4) Downloading Page 2"
WTO        "Downloading Page 2"
;
LFDCOPY    PAGE2, SCRATCH, PAGESZ, ROM
WAIT      NSEC
LFDDNLOD  SCRATCH, PAGESZ
WAIT      NSEC, HK
RECV      2, PAGE2, PAGESZ
WAIT      NSEC, HK
;LOG      1, 1, 2
;
DTG        3, "(5) Downloading Page 3"
WTO        "Downloading Page 3"
;
LFDCOPY    PAGE3, SCRATCH, PAGESZ, ROM
WAIT      NSEC
LFDDNLOD  SCRATCH, PAGESZ
WAIT      NSEC, HK
RECV      2, PAGE3, PAGESZ
WAIT      NSEC, HK
;LOG      1, 1, 2
;
DTG        3, "(6) Downloading Page 4"
WTO        "Downloading Page 4"
;
LFDCOPY    PAGE4, SCRATCH, PAGESZ, ROM
WAIT      NSEC, HK
LFDDNLOD  SCRATCH, PAGESZ
WAIT      NSEC, HK
RECV      2, PAGE4, PAGESZ
WAIT      NSEC, HK
;LOG      1, 1, 2
;
DTG        3, "(7) Downloading Page 5"
WTO        "Downloading Page 5"
;
LFDCOPY    PAGE5, SCRATCH, PAGESZ, ROM
WAIT      NSEC, HK
LFDDNLOD  SCRATCH, PAGESZ
WAIT      NSEC, HK
RECV      2, PAGE5, PAGESZ
WAIT      NSEC, HK
;LOG      1, 1, 2
;
; *****
; * The CRC for Buffer 2 should now equal the CRC reported in HK for ROM *
; *****
;
LOG        1, LFMROM, 1, 2
CHECK      1, ($L2      == 0x1800)
CHECK      1, ($LFMROM == $CRC2)
;
DTG        3, "(8) stp5.5.3.1 completed successfully"
WTO        "stp5.5.3.1 completed successfully"

```

Appendix C. Test Report stp5_5_3_1.rp1

```

1                               55555      55555      333
11                              5          5          3 3
1          ssss ttttt  pppp  555      555          3
1          s      t  p  p    5          5          3
1          sssss  t  pppp   5          5          3
1          s      t  p    5  5      5  5      3 3
1          ssss   t  p    555      555      333
111

```

Ver 01.09 Sat Nov 18 03:43:01 2000 "(0) Resetting"

Addr	Addr	HK-Name	Value
1664-167F	LFDCBUF	8080 7F7F 008A FF75 0000 FFFF 0000 FFFF 0000 FFFF 0000	FFFF 0000
1780-179F	LFDDIAGS	0A32 0932 0832 0732 0606 0532 0406 0332 0232 011B	0000
17A0-17BF		0000 0000 0000 0000 0000 0000 0000 0000 0000 0000	0000
16FE-16FF	LFDSWVER	0109	
16F4-16F5	LFSEBITS1	0000	

Addr	Mask	HK-Bit-Name	Value
16F4	0008	LFDOPERT	0

Addr	Addr	HK-Name	Value
16FC-16FD	LFMROM	D89D	

LFDCRC 0x0000,NBYTES,ROM

Ver 01.09 Sat Nov 18 03:43:04 2000 "(1) Downloading first 64 bytes of ROM to Buffer 1"

Addr	Addr	HK-Name	Value
1664-167F	LFDCBUF	8080 7F7F 0006 FFF9 0000 FFFF 0000 FFFF 0000 FFFF 0000	FFFF 0000
1780-179F	LFDDIAGS	011B 0000 0000 0000 0000 0000 0000 0000 0000 0000	0000
17A0-17BF		0000 0000 0000 0000 0000 0000 0000 0000 0000 0000	0000
16FE-16FF	LFDSWVER	0109	
16F4-16F5	LFSEBITS1	0000	

Addr	Mask	HK-Bit-Name	Value
16F4	0008	LFDOPERT	0

Addr	Addr	HK-Name	Value
16FC-16FD	LFMROM	D89D	

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```

Len  CRC  Buffer          Data
-----
0040 A59F 1          02 01 90 20 7F 02 01 40 02 80 03 20 7F 02 01 80 02 80 0B 20 7F
02 01 60 02 80 13 20 7F 02 01 D0
02 80 1B 20 7F 02 01 E0 02 80 23 20 7F 02 01 F0 02 80 2B 20 7F
02 21 00 02 80 33 FF FF FF FF FF

```

0000 FFFF 2

Ver 01.09 Sat Nov 18 03:43:09 2000 "(2) Downloading Page 0"

LFDCOPY PAGE0,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

Ver 01.09 Sat Nov 18 03:43:14 2000 "(3) Downloading Page 1"

LFDCOPY PAGE1,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

Ver 01.09 Sat Nov 18 03:43:20 2000 "(4) Downloading Page 2"

LFDCOPY PAGE2,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

Ver 01.09 Sat Nov 18 03:43:23 2000 "(5) Downloading Page 3"

LFDCOPY PAGE3,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

Ver 01.09 Sat Nov 18 03:43:26 2000 "(6) Downloading Page 4"

LFDCOPY PAGE4,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

Ver 01.09 Sat Nov 18 03:43:31 2000 "(7) Downloading Page 5"

LFDCOPY PAGE5,SCRATCH,PAGSZ,ROM

LFDDNLOD SCRATCH,PAGSZ

```

Addr Addr HK-Name      Value
-----
16FC-16FD LFMROM          D89D

```

```

Len  CRC  Buffer          Data
-----
0040 A59F 1          02 01 90 20 7F 02 01 40 02 80 03 20 7F 02 01 80 02 80 0B 20 7F
02 01 60 02 80 13 20 7F 02 01 D0
02 80 1B 20 7F 02 01 E0 02 80 23 20 7F 02 01 F0 02 80 2B 20 7F
02 21 00 02 80 33 FF FF FF FF FF

```

```

1800 D89D 2          02 01 90 20 7F 02 01 40 02 80 03 20 7F 02 01 80 02 80 0B 20 7F
02 01 60 02 80 13 20 7F 02 01 D0
02 80 1B 20 7F 02 01 E0 02 80 23 20 7F 02 01 F0 02 80 2B 20 7F

```

```

02 21 00 02 80 33 FF FF FF FF FF
C0 E0 C0 F0 C0 83 C0 82 C0 D0 75 D0 08 90 53 00 12 01 10 D0 D0
D0 82 D0 83 D0 F0 D0 E0 32 FF FF
C0 E0 C0 F0 C0 83 C0 82 C0 D0 75 D0 08 90 57 00 12 01 10 D0 D0
D0 82 D0 83 D0 F0 D0 E0 32 FF FF
C0 E0 C0 F0 C0 83 C0 82 C0 D0 75 D0 08 75 8A 00 75 8C CC 75 8A
18 90 24 60 E0 04 F0 C3 E5 3B 95
81 50 03 85 81 3B D5 30 0E 75 30 32 90 24 61 12 06 A0 90 24 60
E4 F0 D0 D0 D0 82 D0 83 D0 F0 D0

```

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```

E0 32 FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF 90 24 DD 74 1B
F0 A3 74 00 F0 74 31 12 0A 40 32
90 24 DD 74 23 F0 A3 74 00 F0 74 31 12 0A 40 32 90 24 DD 74 2B
F0 A3 74 00 F0 74 31 12 0A 40 32
90 24 DD 74 33 F0 A3 74 00 F0 74 31 12 0A 40 32 E0 F5 34 15 83
E0 F5 33 15 83 E0
F5 31 E5 34 30 E7 13 54 7F 70 46 E5 33 70 42 E5 32 70 3E E5 31
70 3A 02 05 70 C3 E5 33 94 40 E5
34 54 1F F5 34 94 00 40 28 E5 33 94 5B E5 34 94 04 50 1E E5 34
F5 83 E5 33 F5 82 43 83 20 E5 31
F0 A3 E5 32 F0 D2 00 90 24 40 E0 70 1C C2 00 80 18 90 24 D6 12
06 90 90 24 D8 E5 31 F0 A3 E5 32
F0 A3 E5 33 F0 A3 E5 34 F0 22 FF FF FF FF FF FF 75 A8 00 75 B8
00 75 88 00 75 81 51 12 07 50 12
09 80 12 09 B0 12 02 B0 C2 7F D2 AF 75 89 11 75 30 32 D2 8C D2
A9 D2 8D C2 88 D2 A8 C2 8A D2 AA
75 48 01 12 0B 40 12 0C 10 12 07 60 02 01 D0 FF E5 81 64 51 60
08 74 17 12 0A 40 75 81 51 90 24
D1 12 06 90 30 00 05 C2 00 12 02 30 30 01 08 20 00 05 C2 01 12
09 30 30 02 0B 20 00 08 20 01 05
C2 02 12 09 35 30 03 15 20 00 12 20 01 0F 20 02 0C 12 08 00 20
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20 00 09 20 01 06 20 02 03 12 0B 00 02 01 D0 FF 12 06 E0 70 39
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D5 F0 80 0A 30 09 07 C2 09 74 13 12 0A 40 C2 09 12 07 60 22 FF
FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
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25 63 E0 60 0A 14 60 07 F9 78 02
D8 FE D9 FA 90 47 00 E0 22 FF FF FF FF FF FF FF FF FF FF FF FF
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02 FC 74 1C 12 0A 40 22 FF FF FF
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F0 22 FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
90 24 40 7E 00 7F 1C 12 09 F0 22 FF FF FF FF FF FF D2 08 22 FF FF
FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
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E0 54 07 05 E0 F8 90 24 EB 74 01
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77 73 80 06 80 12 80 18 80 24 90
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4E 12 0C 38 E5 4D 90 40 00 F0 02
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37 82 85 38 83 22 FF FF FF FF FF

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FF 78 20 76 00 08 DF FB 90 20 40
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04 7F 00 12 09 F0 90 34 40 7E 04
7F 00 12 09 F0 90 24 60 7E 01 7F 00 12 09 F0 22 BF 00 02 80 01
0E E4 F0 A3 DF FC DE FA 22 FF FF
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E0 C0 F0 90 24 AE E9 23 25 82 F5
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F5 40 E5 3D F5 3F E5 3C 65 40 F5 3E C4 54 0F 65 3E F5 3E C4 54
F0 65 3F F5 3F E5 3E C4 F5 41 54
F0 C3 33 C5 41 54 0F 33 65 3F F5 3C E5 41 65 3E F5 3D E5 40 22
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F0 80 0D 85 39 83 85 3A 82 E4 93 8C 83 8B 82 F0 8C 83 8B 82 A3
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0C 00 01 06 10 00 01 06 12 00 04
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COS DCE BOOT FSW v1.09 Component Test Results
Requirement 5.5.3.1 CRC Background Checking on Memory Regions

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Appendix D. Test Report stp5_5_3_1.rp2

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11         5          5          3 3
1          ssss ttttt pppp 555          555          3
1          s          t  p  p  5          5          3
1          sssss  t  pppp  5          5          3
1          s          t  p  5 5          5 5          3 3
1          ssss  t  p  555  _____ 555  _____ 333  _____
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Ver 01.09 Sat Nov 18 03:43:01 2000 "(0) Resetting"

P O R P A C K E T

80000000

C O M M A N D P A C K E T

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          SN          OPCODE
0446FFFE 04440001 04427D7D 04408282

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Ver 01.09 Sat Nov 18 03:43:04 2000 "(1) Downloading first 64 bytes of ROM to Buffer 1"

C O M M A N D P A C K E T

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C O M M A N D P A C K E T

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C O M M A N D P A C K E T

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          PARM4          PARM3          PARM2          PARM1          PARM0
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                SN          OPCODE
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C O M M A N D P A C K E T

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-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFBF 044C0040 044A3FFF 0448C000
-----

```

```

                SN          OPCODE
0446FFFA 04440005 04425151 0440AEAE
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----

```

```

                SN          OPCODE
0446FFF9 04440006 04427F7F 04408080
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----

```

```

                SN          OPCODE
0446FFF8 04440007 04427F7F 04408080
-----

```

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C O M M A N D P A C K E T

```

-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFE 04540001 0452FBFF 04500400 044E3FFF 044CC000 044AFFFF 04480000
-----

```

```

                SN          OPCODE
0446FFF7 04440008 04427C7C 04408383
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----

```

```

                SN          OPCODE
0446FFF6 04440009 04427F7F 04408080
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----

```

```

                SN          OPCODE
-----

```

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0446FFF5 0444000A 04427F7F 04408080

C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFF 04540000	0452FFFF 04500000	044EFBFF 044C0400	044A3FFF 0448C000
SN	OPCODE			
0446FFF4 0444000B	04425151	0440AEAE		

C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFF 04540000	0452FFFF 04500000	044EFFFF 044C0000	044AFFFF 04480000
SN	OPCODE			
0446FFF3 0444000C	04427F7F	04408080		

C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFF 04540000	0452FFFF 04500000	044EFFFF 044C0000	044AFFFF 04480000
SN	OPCODE			
0446FFF2 0444000D	04427F7F	04408080		

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C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFE 04540001	0452FBFF 04500400	044E3FFF 044CC000	044AFBFF 04480400
SN	OPCODE			
0446FFF1 0444000E	04427C7C	04408383		

C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFF 04540000	0452FFFF 04500000	044EFFFF 044C0000	044AFFFF 04480000
SN	OPCODE			
0446FFF0 0444000F	04427F7F	04408080		

C O M M A N D P A C K E T

PARM4	PARM3	PARM2	PARM1	PARM0
045AFFFF 04580000	0456FFFF 04540000	0452FFFF 04500000	044EFFFF 044C0000	044AFFFF 04480000
SN	OPCODE			
0446FFEF 04440010	04427F7F	04408080		

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```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFBFF 044C0400 044A3FFF 0448C000
-----
      SN              OPCODE
0446FFEE 04440011 04425151 0440AEAE
-----

```

```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OPCODE
0446FFED 04440012 04427F7F 04408080
-----

```

```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OPCODE
0446FFEC 04440013 04427F7F 04408080
-----

```

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```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFE 04540001 0452FBFF 04500400 044E3FFF 044CC000 044AF7FF 04480800
-----
      SN              OPCODE
0446FFEB 04440014 04427C7C 04408383
-----

```

```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFBFF 044C0400 044A3FFF 0448C000
-----
      SN              OPCODE
0446FFEA 04440015 04425151 0440AEAE
-----

```

```

-----
                          C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OPCODE
0446FFE9 04440016 04427F7F 04408080
-----

```

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```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OP CODE
0446FFE8 04440017 04427F7F 04408080
-----

```

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```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFE 04540001 0452FBFF 04500400 044E3FFF 044CC000 044AF3FF 04480C00
-----
      SN              OP CODE
0446FFE7 04440018 04427C7C 04408383
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFBFF 044C0400 044A3FFF 0448C000
-----
      SN              OP CODE
0446FFE6 04440019 04425151 0440AEAE
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OP CODE
0446FFE5 0444001 04427F7F 04408080
-----

```

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```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFE 04540001 0452FBFF 04500400 044E3FFF 044CC000 044AEFFF 04481000
-----
      SN              OP CODE
0446FFE4 0444001B 04427C7C 04408383
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4          PARM3          PARM2          PARM1          PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN              OP CODE
0446FFE3 0444001C 04427F7F 04408080
-----

```

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```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFBFF 044C0400 044A3FFF 0448C000
-----
      SN      OPCODE
0446FFE2 0444001D 04425151 0440AEAE
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN      OPCODE
0446FFE1 0444001E 04427F7F 04408080
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN      OPCODE
0446FFE0 0444001F 04427F7F 04408080
-----

```

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```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFE 04540001 0452FBFF 04500400 044E3FFF 044CC000 044AEBFF 04481400
-----
      SN      OPCODE
0446FFDF 04440020 04427C7C 04408383
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN      OPCODE
0446FFDE 04440021 04427F7F 04408080
-----

```

```

-----
C O M M A N D   P A C K E T
-----
      PARM4      PARM3      PARM2      PARM1      PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
      SN      OPCODE
0446FFDD 04440022 04427F7F 04408080
-----

```

C O M M A N D P A C K E T

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```

-----
                PARM4                PARM3                PARM2                PARM1                PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFBFF 044C0400 044A3FFF 0448C000
-----
                SN                OPCODE
0446FFDC 04440023 04425151 0440AEAE
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4                PARM3                PARM2                PARM1                PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
                SN                OPCODE
0446FFDB 04440024 04427F7F 04408080
-----

```

C O M M A N D P A C K E T

```

-----
                PARM4                PARM3                PARM2                PARM1                PARM0
045AFFFF 04580000 0456FFFF 04540000 0452FFFF 04500000 044EFFFF 044C0000 044AFFFF 04480000
-----
                SN                OPCODE
0446FFDA 04440025 04427F7F 04408080
-----

```

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